

<text><list-item><list-item><list-item><list-item>









Critical Sections		
Execution of p ₁	Execution of p ₂	
… load R1, balance load R2, amount		
Timer interrupt		
	load R1, balance	
	load R2, amount	
	sub R1, R2	
	store R1, balance	
Timer interrupt		
add R1, R2		
store R1, balance		



Slide 8-9 Critical Sections Mutual exclusion: Only one process can be in the critical section at a time There is a <u>race</u> to execute critical sections The sections may be defined by different code in different processes ... cannot easily detect with static analysis Without mutual exclusion, results of multiple execution are not <u>determinate</u> Need an OS mechanism so programmer can

resolve races

Some Possible Solutions Disable interrupts Software solution – locks Transactions FORK(), JOIN(), and QUIT(Terminate processes with QUIT() to synchronize Create processes whenever critical section is complete ... something new ...















	Deadlock (2)	Slide 8-18
	<pre>shared boolean lock1 = FALSE; shared boolean lock2 = FALSE; shared list L;</pre>	
	Code for p ₁ Code	e for p ₂
	/* Enter CS to delete elt */ /* E enter(lock1); en <delete element="">;</delete>	 nter CS to update len */ ter(lock2); <update length="">;</update>
-	<pre><intermediate computation="">; <i *="" <update="" cs="" enter="" enter(lock2);="" len="" length="" to="" update="">; <a *="" both="" cs="" ex<="" exit="" exit(lock1);="" exit(lock2);="" pre=""></i></intermediate></pre>	<pre>ntermediate computation> nter CS to add elt */ ter(lock1); dd element>; /* Exit both CS */ it(lock2); it(lock1);</pre>













Solution Assumptions

Slide 8-25

- Memory read/writes are indivisible (simultaneous attempts result in some
 - arbitrary order of access)
- There is no priority among the processes
- Relative speeds of the processes/processors is unknown
- Processes are cyclic and sequential

Dijkstra Semaphore Definition Slide 8-26 Classic paper describes several software attempts to solve the problem (see problem 4, Chapter 8) Found a software solution, but then proposed a simpler hardware-based solution A <u>semaphore</u>, s, is a nonnegative integer variable that can only be changed or tested by these two indivisible functions: V(s): [s = s + 1] P(s): [while(s == 0) {wait}; s = s - 1]







































```
Implementing Semaphores:
                                               Slide 8-46
      enter() & exit()
class semaphore {
 int value;
public:
  semaphore(int v = 1) { value = v; };
  P(){
   disableInterrupts();
   while(value == 0) {
     enableInterrupts();
     disableInterrupts();
    }
   value--;
   enableInterrupts();
  };
 V(){
   disableInterrupts();
   value++;
   enableInterrupts();
  };
};
```







